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For introductory digital logic design or computer engineering courses in electrical and computer engineering or computer science at the sophomore- or junior-level. Many recent texts place instructors in the difficult position of choosing between authoritative, state-of-the-art coverage and an approach that is highly supportive of student learning.

Digital Logic Circuit Analysis and Design - Pearson

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Digital Logic Circuit Analysis & Design Nelson, Nagle, Carroll, and Irwin; Prentice Hall, 1995, p. 187. 3.5.1· Guidelines for Simplifying Functions Using K-maps There are five important points to keep in mind when simplifying functions on K-maps: 1. Each square (minterm) on a K-map of two variables has two squares

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